

### **Amendments to the Specification**

Please replace the paragraphs on page 2, line 13 through page 4, line 12, with the following updated version:

#### **--Cross-Reference to Related Applications**

This application claims priority to Provisional Application Serial No. 60/177,812, filed on January 25, 2000.

The following commonly owned applications and patents are hereby incorporated by reference for all purposes:

United States Patent Application Serial No. 08/940,419, filed September 30, 1997, entitled "Installation and Removal of Components of a Computer" by Sompong P. Olarig, Kenneth A. Jansen, and Paul A. Santler, issued August 1, 2000, as U.S. Patent No. 6,098,132;

United States Patent Application Serial No. 08/940,282, filed September 30, 1997, entitled "Fault Tolerant Memory" by Paul A. Santler, Kenneth A. Jansen, and Sompong P. Olarig.

United States Patent Application Serial No. 09/303,369, filed April 30, 1999, entitled "Replacement, Upgrade and/or Additional of Hot-Pluggable Components in a Computer System" by Theodore F. Emerson, Vincent Nguyen, Peter Michels, and Steve Clohset; and

United States Patent Application Serial No. [[\_\_\_\_]]09/769,956, filed January 25, 2001~~concurrently herewith~~, entitled “Memory Sub-System Error Cleansing” by William J. Walker, and John M. MacLaren;

United States Patent Application Serial No [[\_\_\_\_]]09/769,716, filed January 25, 2001~~concurrently herewith~~, entitled “Hot Replace Power Control Sequence Logic” by John M. MacLaren, Jerome J. Johnson, Robert A. Lester, Gary J. Piccirillo, John E. Larson, and Christian H. Post;

United States Patent Application Serial No. [[\_\_\_\_]]09/769,783, filed January 25, 2001~~concurrently herewith~~, entitled “Hot-Replace of Memory” by John E. Larson, John M. MacLaren, Jerome J. Johnson, Gary J. Piccirillo, Robert A. Lester, Christian H. Post, Jefferey Galloway, Anisha Anand, Ho M. Lai, and Eric Rose;

United States Patent Application Serial No. [[\_\_\_\_]]09/769,978, filed January 25, 2001~~concurrently herewith~~, entitled “Hot-Upgrade/Hot-Add Memory” by Jerome J. Johnson, John M. MacLaren, Robert A. Lester, John E. Larson, Gary J. Piccirillo, Christian H. Post, Jefferey Galloway, Ho M. Lai, Anisha Anand, and Eric Rose

United States Patent Application Serial No. [[\_\_\_\_]]09,769,957, filed January 25, 2001 ~~concurrently herewith~~, entitled “Raid Memory” by John M. MacLaren, Paul Santeler, Kenneth A. Jansen, Sompong P. Olarig, Robert A. Lester, Patrick L. Ferguson, John E. Larson, Jerome J. Johnson, and Gary J. Piccirillo;

United States Patent Application Serial No. [[\_\_\_\_]]09/769,833, filed January 25, 2001~~concurrently herewith~~, entitled “Technique for Identifying Multiple Circuit Components” by John M. MacLaren and John E. Larson;

United States Patent Application Serial No. [[\_\_\_\_]]09/769,958, filed January 25, 2001~~concurrently herewith~~, entitled “Memory Data Verify Operation” by Robert A. Lester, John M. MacLaren, Patrick L. Ferguson, and John E. Larson;

United States Patent Application Serial No. [[\_\_\_\_]]09/769,959, filed January 25, 2001~~concurrently herewith~~, entitled “Real-Time Hardware Memory Scrubbing” by John E. Larson, John M. MacLaren, Robert A. Lester, Gary Piccirillo, Jerome J. Johnson, and Patrick L. Ferguson;

United States Patent Application Serial No. [[\_\_\_\_]]09/770,101, filed January 25, 2001~~concurrently herewith~~, entitled “Hot-Plug Memory Cartridge Power Control Logic” by Ho M. Lai and John M. MacLaren;

United States Patent Application Serial No. [[\_\_\_\_]]09/770,100, filed January 25, 2001~~concurrently herewith~~, entitled “Removable Memory Cartridge System for Use With a Server or Other Processor-Based Device” by Brett D. Roscoe, George D. Megason, and Christian H. Post;[[.]]--